



Fig. 5. Load voltage versus Inductor current (I_{L2}): (enlargement)

Fig.4. shows the magnitude of the current and voltage supplied to the load (R_2 , R_Y and L_3). Before the $t=2.5s$, there is no current or voltage supplied to the load, and due to the switch 6 and 7 are still in the OFF state. In Fig. 5 (magnification of Fig. 4) it appears that after the $t=2.5s$, the switch 6 in the ON state, then came the current and voltage supplied to the load, The current and voltage appears in the same phase condition..

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